

REMARKS

In the advisory action mailed on 31 December 2002 (Paper No. 21), the Examiner stated: "in claims 1 and 3, if the applicant in amend (D) changes the alternative OR language to AND then the amend of those claims are over prior art and the final rejection will be withdrawn." The applicant agrees to the Examiner's suggested amendment and will hereby amend the claims as stated in the quoted statement. Therefore, the instant above amendment changes the alternative OR language to AND in claims 1 and 3. Accordingly, it is respectfully submitted, the final rejection should be withdrawn and the case should be passed to allowance.

In view of the above, it is submitted that the claims of this application are in condition for allowance, and early issuance thereof is solicited.

A fee of \$110.00 is incurred by the filing of a Petition for a one-month extension of time. Applicant's check drawn to the order of Commissioner accompanies this Amendment. Should the check become lost, be deficient in payment, or should other fees be incurred, the Commissioner is authorized to charge Deposit Account No. 02-4943 of Applicant's undersigned attorney in the amount of such fees.

Respectfully submitted,



Robert E. Bushnell,
Attorney for the Applicant
Registration No.: 27,774

1522 K Street N.W., Suite 300
Washington, D.C. 20005
(202) 408-9040

Folio: P55281
Date: 1/28/03
I.D.: REB/RHS

MARKED-UP VERSION OF AMENDMENTS

IN THE CLAIMS

Please amend claims 1 and 3, as follows:

1 1 (amended twice). A flat panel display apparatus for receiving display information
2 including video data and synchronizing data from a host processing digital data in a serial
3 digital communication, said display apparatus adapted for operation without need for any
4 analog-to-digital converter (ADC) [or] and phase-locked loop (PLL) circuit for signal
5 conversion, said display apparatus comprising:

6 a receiver for reconstructing said display information;

7 a synchronizing signal generator for generating a synchronizing signal by extracting
8 the synchronizing data from said reconstructed display information;

9 a digital-to-analog converter (DAC) for converting said video data to a corresponding
10 analog video signal; and

11 an output terminal for externally transferring said synchronizing signal and analog
12 video signal to an analog display apparatus.

1 3 (amended twice). A digital data processing system including a host computer for
2 processing digital data and a flat panel display apparatus for displaying display
3 information received from said host computer, said system comprising:

4 a transmitter connected to said host to transfer digital display information as serial
5 data;
6 a receiver for reconstructing said digital display information;
7 a synchronizing signal generator for generating a synchronizing signal by extracting
8 synchronizing data from said reconstructed display information;
9 a digital-to-analog converter (DAC) for converting video data to a corresponding
10 analog video signal; and
11 an output terminal for externally transferring said synchronizing signal and said
12 analog video signal to an analog display apparatus;
13 wherein said flat panel display apparatus includes said receiver, said synchronizing signal
14 generator, and said output terminal; and
15 wherein said flat panel display apparatus does not utilize any analog-to-digital converter
16 (ADC) [or] and phase-locked loop (PLL) circuit for signal conversion.